	Hits	S arch Text	DBs
1	125	(((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock)))	US-PGPUB; USPAT
2	24	((((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock)))) same (programmable delay adj line)	US-PGPUB; USPAT
3	10	(((((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock)))) same (programmable delay adj line)) same (phase adj detector)	US-PGPUB; USPAT
4	3	(((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock))) same (divide adj2 two)	US-PGPUB; USPAT
5	18	(((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock))) same (frequency adj divider)	US-PGPUB; USPAT
6	4	(((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock))) same (frequency adj divider) same (phase adj detector)	US-PGPUB; USPAT
7	13	dll with fpga	US-PGPUB; USPAT
8	8	(delay$2 adj lock$2 adj loop) with fpga	US-PGPUB; USPAT
9	16	(dll with fpga) or ((delay$2 adj lock$2 adj loop) with fpga)	US-PGPUB; USPAT
10	5	(dll with fpga) and ((delay$2 adj lock$2 adj loop) with fpga)	US-PGPUB; USPAT
11	335	programmable adj delay adj line	US-PGPUB; USPAT
12	7	(programmable adj delay adj line) same (dll or (delay$2 adj lock$2 adj loop))	US-PGPUB; USPAT
13	21	(clock adj tree) same (dll or (delay$2 adj lock$2 adj loop))	US-PGPUB; USPAT

	Hits	S arch Text	DBs
14	5	(clock adj doubler) same (dll or (delay$2 adj lock$2 adj loop))	US-PGPUB; USPAT
15	16	(clock adj doubler) and (dll or (delay$2 adj lock$2 adj loop))	US-PGPUB; USPAT
16	1	deskew with (clock adj2 out)	US-PGPUB; USPAT
17	3	(deskew$3 adj3 clock) same (clock adj2 out)	US-PGPUB; USPAT
18	0	(deskew$3 adj3 clock) same (("0" or zero) adj ns)	US-PGPUB; USPAT
19	27	("0 ns") with (clock) with reference	US-PGPUB; USPAT
20	14	programmable adj (dll or (delay adj2 loop))	US-PGPUB; USPAT
21	1505	(external adj clock) near4 (internal adj clock)	US-PGPUB; USPAT
22	97	select$4 near4 ((external adj clock) near4 (internal adj clock))	US-PGPUB; USPAT
23	72	"one" adj3 ((external adj clock) near4 (internal adj clock))	US-PGPUB; USPAT
24	32	"one" adj ((external adj clock) near4 (internal adj clock))	US-PGPUB; USPAT
25	0	("one" adj ((external adj clock) near4 (internal adj clock))) same (dll or (delay adj lock$2 adj loop))	US-PGPUB; USPAT
26	3	(select$4 near4 ((external adj clock) near4 (internal adj clock))) same (dll or (delay adj lock$2 adj loop))	US-PGPUB; USPAT
27	2	("one" adj ((external adj clock) near4 (internal adj clock))) and (dll or (delay adj lock$2 adj loop))	US-PGPUB; USPAT
28	115	((external adj clock) near4 (internal adj clock)) with reference	US-PGPUB; USPAT
29	57	(((external adj clock) near4 (internal adj clock)) with reference) and (dll or (delay adj lock$2 adj loop))	US-PGPUB; USPAT
30	1	(((external adj clock) near4 (internal adj clock)) with reference) and (programmable with (dll or (delay adj lock$2 adj loop)))	US-PGPUB; USPAT
31	32	(select$4 near2 coupl$3) same (dll or (delay adj lock$2 adj loop))	US-PGPUB; USPAT

	Hits	Search T xt	DBs
32	2	(programmably adj coupl$3) same (dll or (delay adj lock$2 adj loop))	US-PGPUB; USPAT
33	2	(programmably adj select$3) same (dll or (delay adj lock$2 adj loop))	US-PGPUB; USPAT
34	2	(programmable) same (dll or (delay adj lock$2 adj loop))	EPO; JPO
35	19	(programmable) same (dll or (delay adj lock$2 adj loop))	DERWENT; IBM_TDB
36	8	(input adj select$3) with (dll or (delay adj lock$2 adj loop))	US-PGPUB; USPAT
37	28	(input adj2 select$3) with (dll or (delay adj lock$2 adj loop))	US-PGPUB; USPAT
38	5	(input near2 select$3 near2 clock) with (dll or (delay adj lock$2 adj loop))	US-PGPUB; USPAT
39	0	((programmabl$5 adj delay) same (feedback adj delay)) and ((clock near2 doubl$3) same (clock adj tree))	US-PGPUB; USPAT
40	0	((programmabl$5 adj delay) same (feedback adj delay)) and ((clock near2 doubl$3) and (clock adj tree))	US-PGPUB; USPAT
41	0	((programmabl$5) same (feedback adj delay)) and ((clock near2 doubl$3) same (clock adj tree))	US-PGPUB; USPAT
42	0	((programmabl$5) same (feedback adj delay)) and ((clock near2 doubl$3) and (clock adj tree))	US-PGPUB; USPAT
43	0	((programmabl$5) and (feedback adj delay)) and ((clock near2 doubl$3) and (clock adj tree))	US-PGPUB; USPAT
44	0	((programmabl$5 adj delay) same (feedback)) and ((clock near2 doubl$3) and (clock adj tree))	US-PGPUB; USPAT
45	33	((clock near2 doubl$3) and (clock adj tree))	US-PGPUB; USPAT
46	13	(((clock near2 doubl$3) and (clock adj tree))) and programmabl$3	US-PGPUB; USPAT
47	8	((clock near2 doubl$3) and (clock adj tree)) and fpga	US-PGPUB; USPAT
48	0	((programmabl$5 adj delay) same (feedback adj delay)) and fpga	US-PGPUB; USPAT
49	4	((programmabl$5 adj delay) same (feedback adj delay))	US-PGPUB; USPAT

	Hits	Search Text	DBs
50	118	((programmabl$5 adj delay) same (feedback))	US-PGPUB; USPAT
51	9	((programmabl$5 adj delay) same (clock near2 doubl$3))	US-PGPUB; USPAT
52	48	((((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock)))) same (programmable delay adj line)	US-PGPUB; USPAT
53	24	(programmable adj delay adj line) same (dll or (delay$2 adj lock$2 adj loop))	US-PGPUB; USPAT
54	18	(((clock near2 doubl$3) and (clock adj tree))) and programmabl$3	US-PGPUB; USPAT
55	0	((((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock)))) same (programmable delay adj line)	EPO; JPO
56	1	(programmable adj delay adj line) same (dll or (delay$2 adj lock$2 adj loop))	EPO; JPO
57	0	(((clock near2 doubl$3) and (clock adj tree))) and programmabl$3	EPO; JPO
58	1	chang.xa. and plants.in.	US-PGPUB; USPAT
59	26	(programmable adj delay adj line) same (dll or (delay$2 adj lock$2 adj loop))	US-PGPUB; USPAT
60	23	programmable adj (dll or (delay adj2 loop))	US-PGPUB; USPAT
61	1	(programmable adj delay adj line) same (dll or (delay$2 adj lock$2 adj loop))	EPO; JPO
62	1	programmable adj (dll or (delay adj2 loop))	EPO; JPO